SRINIVASA EDUCATIONAL SOCIETY'S

PAGE INSTITUTE OF TECHNOLOGY & SCIENCES

(AUTONOMOUS)

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Lr No: PACE (A)/ECS/2025-2026/IE/10

01/09/2025

CIRCULAR

All the III B.Tech (R23) I Semester Students are hereby informed that the first mid examinations is Scheduled from 09/09/2025. The Time table of examinations is as given below.

The pattern of the Descriptive Examination: Three either or type questions of equal weightage of 10 marks for 90 minutes.

Timing of the Descriptive Examination

: 12:00 pm to 1:30 pm

Timing of the Online Examination

: 9:30 am onwards

Branch	Date & Day				
	09-09-2025 (Tue Day)	10-09-2025 (Wed Day)	11-09-2025 (Thu Day)	12-09-2025 (Fri Day)	
CIVIL'	Design and Drawing of Reinforced Concrete Structures	Engineering Hydrology	Geotechnical Engineering	Principles of Database Management Systems	
EEE	Power Electronics	Digital Circuits	Power Systems- II	Signals and Systems	
ME	Machine Tools & Metrology	Thermal Engineering	Design of Machine Elements	Renewable Energy Technologies	
ECE	Analog and Digital IC Applications	Digital Communications	Antennas and Wave Propagation	Optical Communications	
CSE	Data Warehousing and Data Mining	Computer Networks	Formal Languages and Automata Theory	Introduction to Simulation Software	
CSIT	Data Warehousing and Data Mining	Computer Networks	Automata Theory and Compiler Design	Entrepreneurship Development and Venture Creation	
IT.	Data Warehousing and Data Mining	Computer Networks	Advanced Java	Entrepreneurship Development and Venture Creation	

Branch	Date & Day				
	09-09-2025 (Tue Day)	10-09-2025 (Wed Day)	11-09-2025 (Thu Day)	12-09-2025 (Fri Day)	
CSE(IOT& CSBT)	Designing the IOT	Wireless Sensor Networks	Cryptography & Network Security	Entrepreneurship Development and Venture Creation	
AIDS	Data Warehousing and Data Mining	Principles of Machine Learning	Introduction to Data Science	Entrepreneurship Development and Venture Creation	
AIML	Deep Learning	Computer Networks	Natural Language Processing	Entrepreneurship Development and Venture Creation	
CSE (RL)	Data Warehousing and Data Mining	Computer Networks	Formal Languages and Automata Theory	Introduction to Simulation Software	
EE (VLSI DT)	Basic VLSI Design	Digital System Design through HDL	Linear and Digital IC Applications	Introduction to Micro Processor and Micro controllers	

NOTE:

i) Any Omissions or clashes in this Time Table may please be informed to the Controller of Examinations immediately

ii) Even if government declares holiday on any of the above dates, the examinations shall be conducted as usual

CONTROLLER OF EXAMINATIONS
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PACE INSTITUTE OF TECHNOLOGY & SCIENCES
(AUTONOMOUS)
VALLUR, ONGOLE-523 272. ANDHRA PRADESH

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: Notice board at Exam Cell & System

: Deputy Controller of Examinations

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